

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390P10924	Application Number: 09/783,779
Page 1 of 1		First Named Inventor: Srinivas Chennupaty, et al.	Examiner: Not yet assigned
Confirmation No. 3051		Filing Date: February 14, 2001	Art Unit: 2183

**OTHER ART - NO PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Transl ation <sup>6</sup>
(CSK)		ABBOTT, et al., "Broadband Algorithms with the MicroUnity Mediaprocessor," MicroUnity Systems Engineering, Inc., Proceedings of Compcon, IEEE, 1996, pp. 349-354.	
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(CSK)		DIEFENDORFF, K., et al., "AltiVec Extension to PowerPC Accelerates Media Processing," IEEE, #0272-1732/00, 2000, pp. 85-95.	
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(CSK)		HAYES, et al., "MicroUnity Software Development Environment," MicroUnity Systems Engineering, Inc., Proceedings of Compcon, IEEE, 1996, pp. 341-348.	
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(CSK)		INTEL CORPORATION, "IA-32 Intel® Architecture Software Developer's Manual, Vol. I: Basic Architecture," 2002, 21 pgs. total.	
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(CSK)		WANG, et al., "A Processor Architecture for 3D Graphics Calculations," Computer Motion, Inc., Goleta, CA, 23 pgs.	
Examiner Signature	<i>Ken S. Kim</i>		Date Considered 5-17-05

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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